<u>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</u>

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Reply to office action of May 17, 2005 SEP 1 5 2005

PATENT Docket: CU-3455

In re Application:

Chang Yeol LEE et al.

GRP ART UNIT: 2813

Serial No:

10/712,921

Application Serial No. 10/712,921

Ex.: Chen, Jack S. J.

Filed:

November 13, 2003

For:

METHOD FOR REDUCING POLY-DEPLETION IN DUAL GATE

CMOS FABRICATION PROCESS

Certification under 37 C.F.R. §1.8(a)

The USPTO Central Fax No. (571) 273-8300 Date of Fax Transmittal: September 15, 2005

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted to the United States Patent and Trademark Office to the fax number and on the date indicated above.

W. William Park, Reg. No. 55,523

The Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REQUEST FOR ONE-MONTH EXTENSION OF TIME

Dear SIr:

With reference to the Office Action dated May 17, 2005, Applicants hereby request a one-month extension of the term for response, which thereby extends the end date of the current term from August 17, 2005 to <u>September 17, 2005</u>.

Please charge \$120.00 to Deposit Account No. 12-0400 to cover the cost of the one-month extension. No other fee is believed to be required. However, if this is not the case, please charge any additional fee or credit any overpayment to the same Deposit Account No. 12-0400.

Respectfully submitted,

Dated: September 15, 2005

V. William Park, Reg. No. 55,523

Ladas & Parry LLP

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224 South Michigan Avenue

01 FC:1251 120.00 DA

Chicago, Illinois 60604 (312) 427-1300